Project	SDHCAL
Group	CALICE
Project responsable	Vincent Boudry

Tracks changes

- Milestones

DIF firmware upgrade to version 2	working on
LDA library	working on
Non regression tests	working on

- Todo list

provide to Guillaume Baulieu the Spiroc fields for DB
write the Online server specification
sending Slow control to ASU (to merge)
receiving data on trigger from ASU (to test)
check CRC computation/global trigger counter (bypassed)
install and test the XDAQ user interface (to document)
provide registers to libLDA for assynchronous slow control (v2)

- Done

12-2010	Transition's documentation
1-2011	Test servers dupplication
2-2011	SLC55 port (no libcap)
3-2011	Use callback with libpcap to bypass packets copy
4-2001	Use threads and share data between them
5-2001	Send configuration and read out data
6-2001	Port library software on ECAL

- Troubles

Working on ASUv2 since Lyon in fact use ASUv3
`send_FC_DIF_reset` doesn't works on DHCAL
`send_FC_DIF_read_debug_fifo` doesn't works on DIFv2
LDA suspected to not propagate all read orders
libcap need a kernel patch on SLC55 (must use setuid root)